

FIG. 1

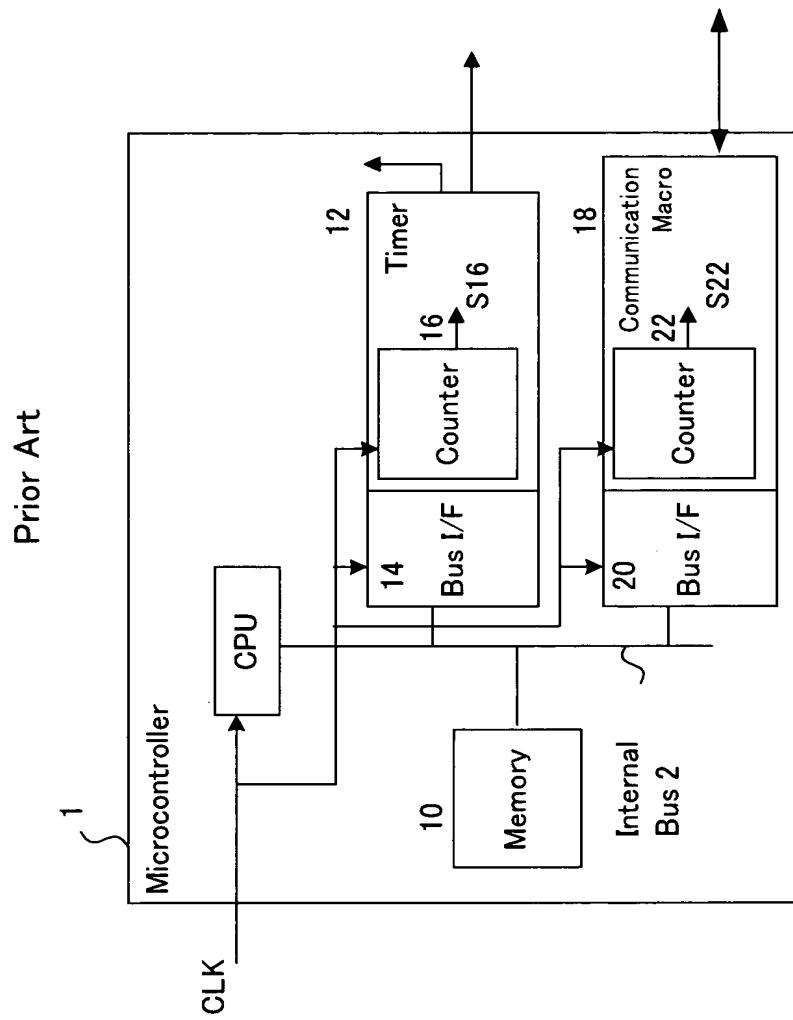
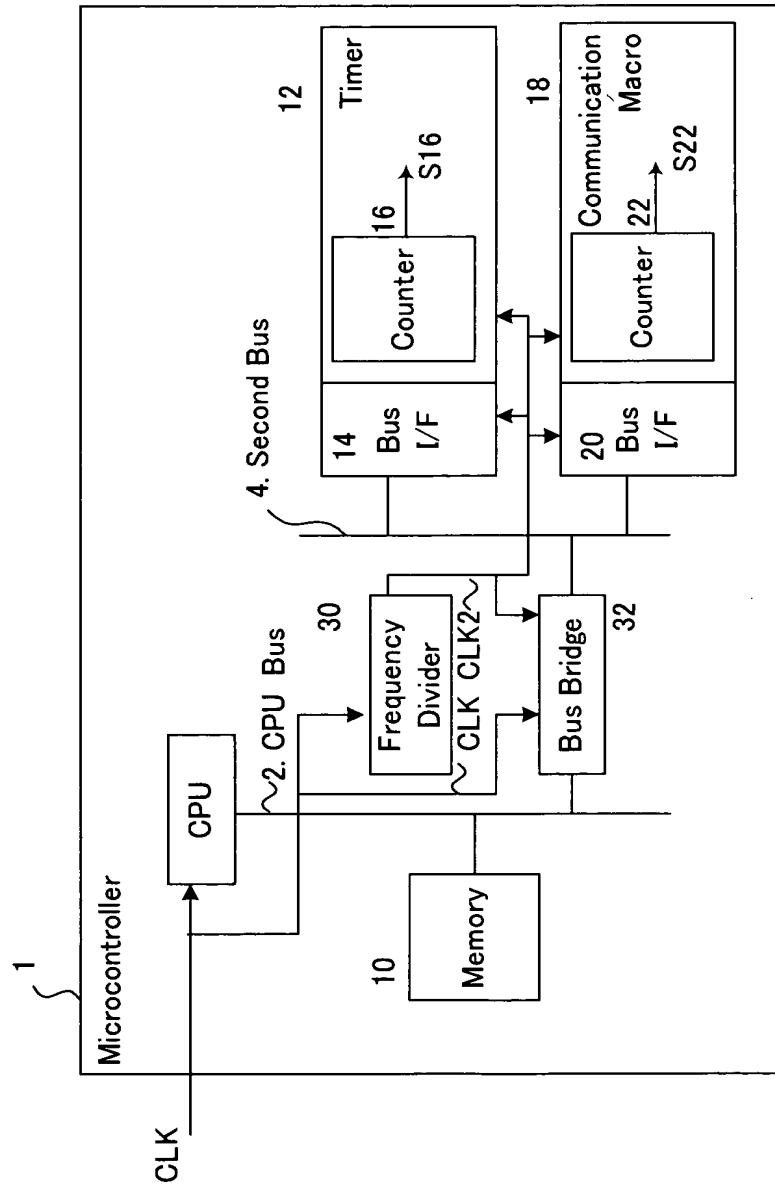


FIG. 2

Prior Art



**FIG. 3**

**Microcontroller of Embodiment**

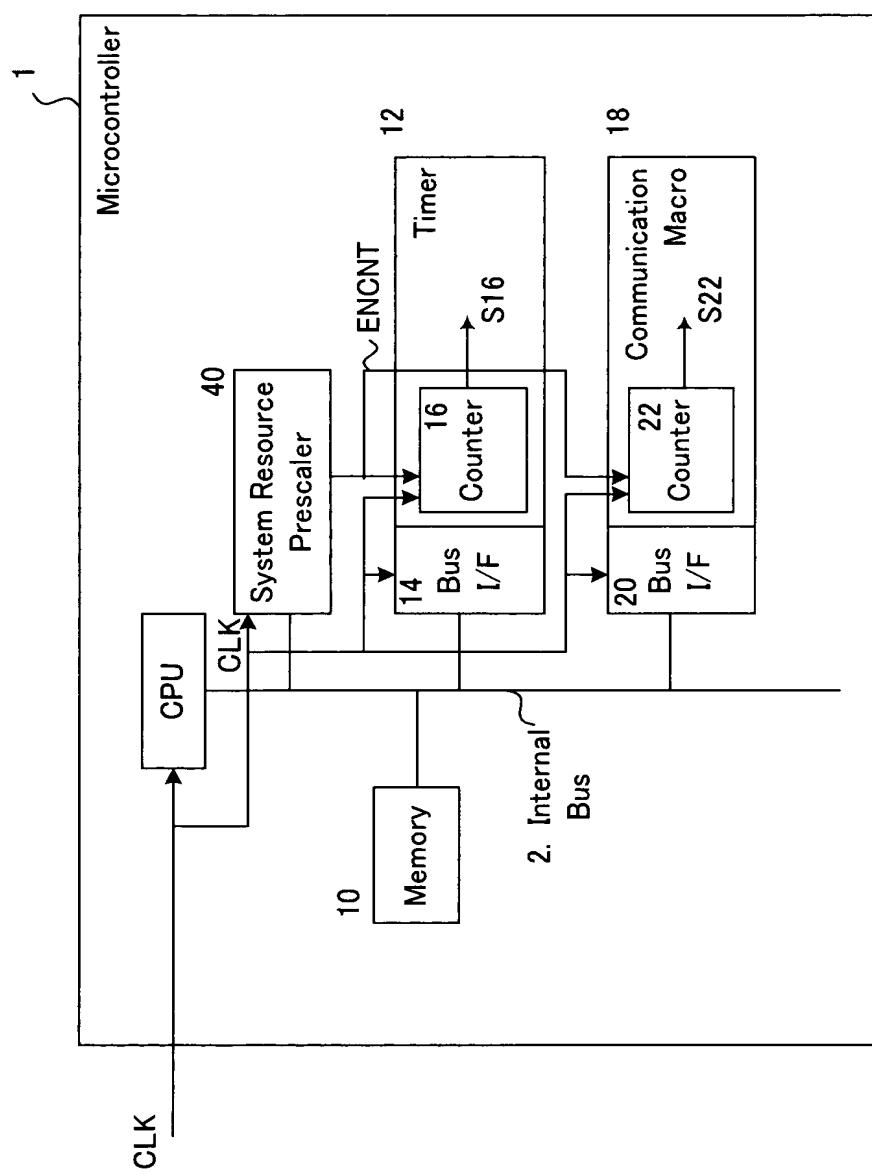
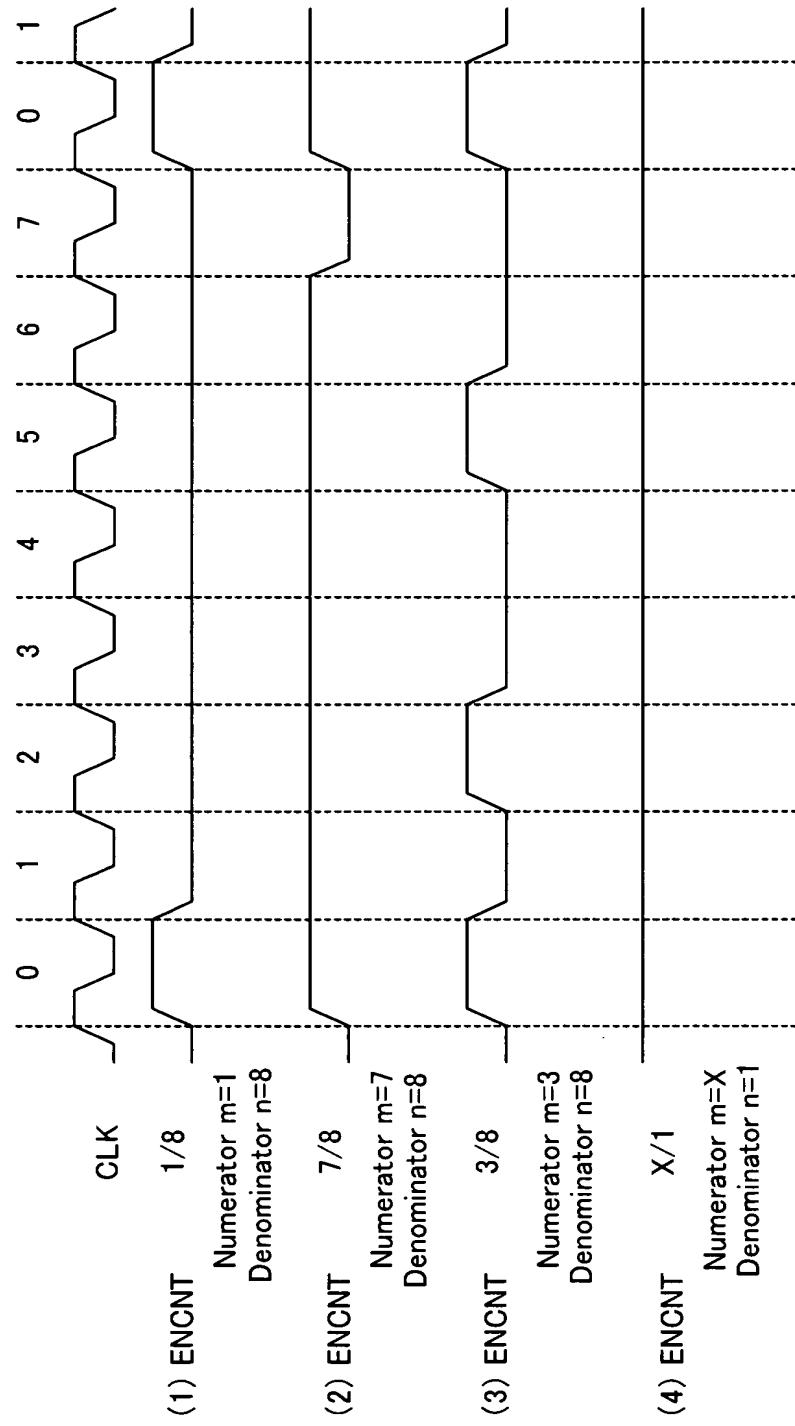
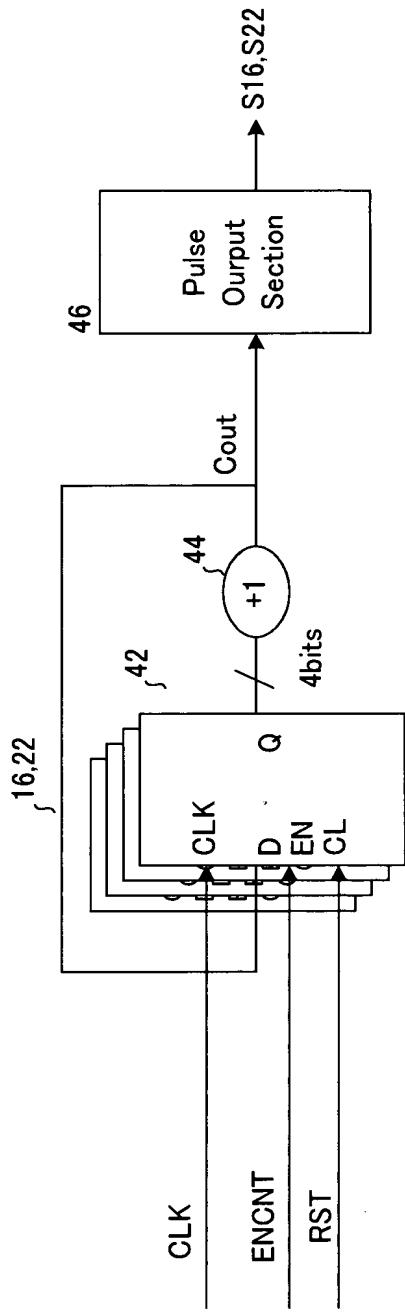


FIG. 4

Example of ENCNT



**FIG. 5A**



**FIG. 5B**

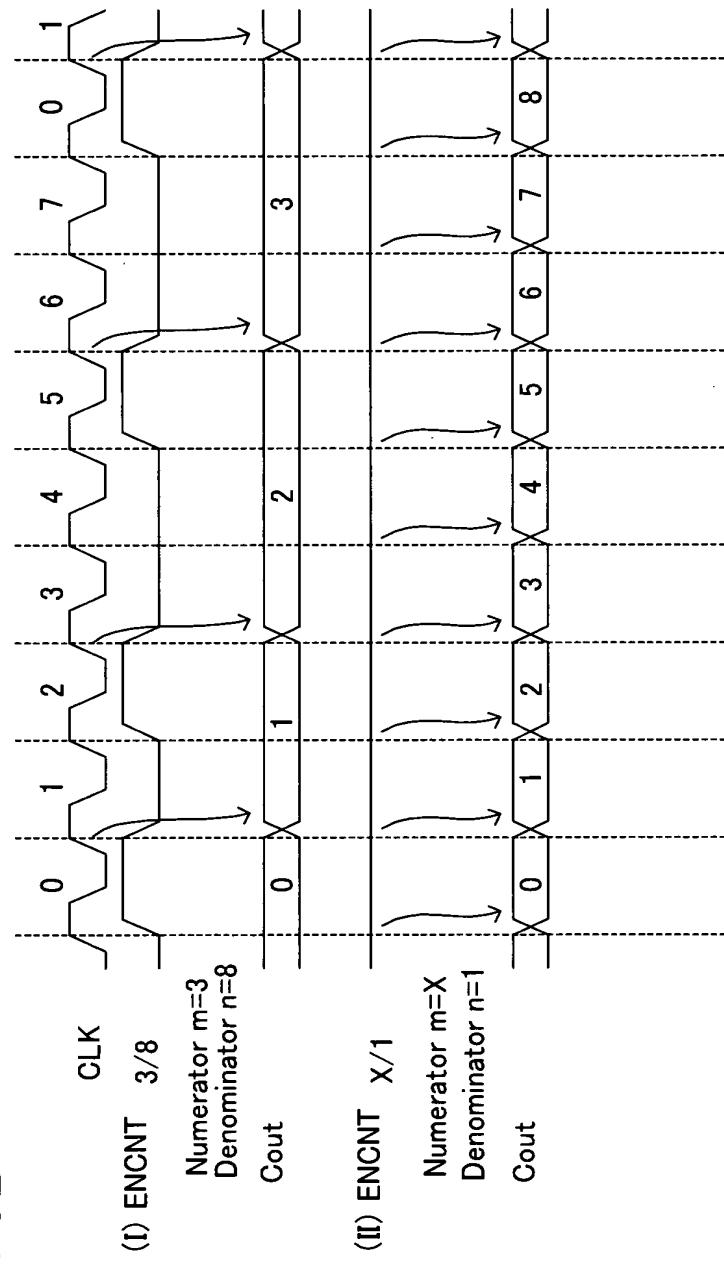
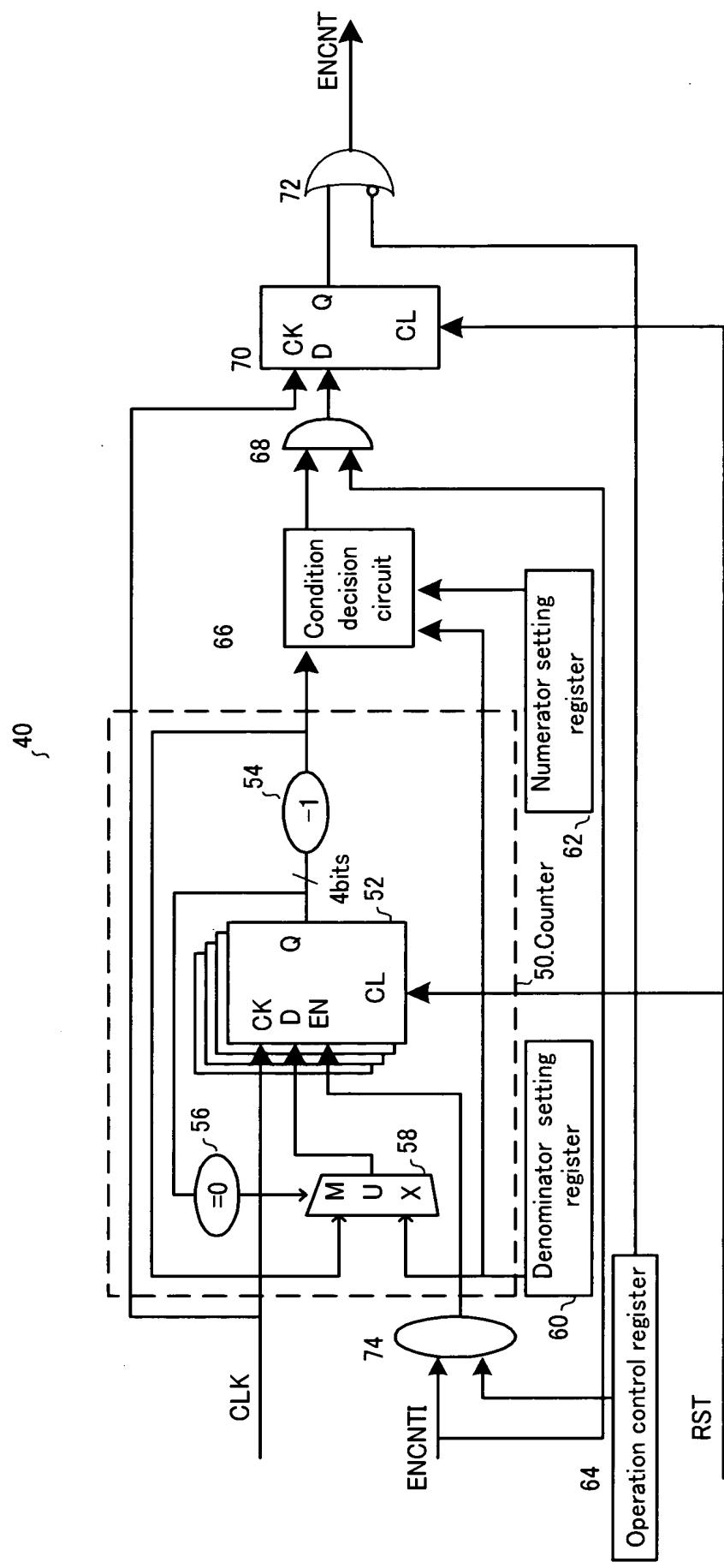


FIG. 6



**FIG. 7A**

Denominator/Numerator Setting Register 60, 62

	7	6	5	4	3	2	1	0	Initial Data
	Nume3	Nume2	Nume1	Nume0	Denom3	Denom2	Denom1	Denom0	
	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Operation Control Register 64									
7	6	5	4	3	2	1	0		Initial Data
—	—	—	—	—	—	—	—	—	00000000
—	—	—	—	—	—	—	—	—	R/W

**FIG. 7B**

Decision Table of Condition Decision Circuit

n-1 \ m-1	0	1	2	3	4	5	6	7
0	0	0	0	0	0	0	0	0
1	—	0-1	0-1	0,2	0,2	0,3	0,3	0,4
2	—	—	0-2	0-2	0,1,3	0,2,4	0,2,5	0,2,5
3	—	—	—	0-3	0-3	0,1,3,4	0,1,3,5	0,2,4,6
4	—	—	—	—	0-4	0-4	0,1,2,4,5	0,1,2,4,6
5	—	—	—	—	—	0-5	0-5	0,1,2,4,5,6
6	—	—	—	—	—	—	0-6	0-6
7	—	—	—	—	—	—	—	0-7

3/8

FIG. 8

Modified Example

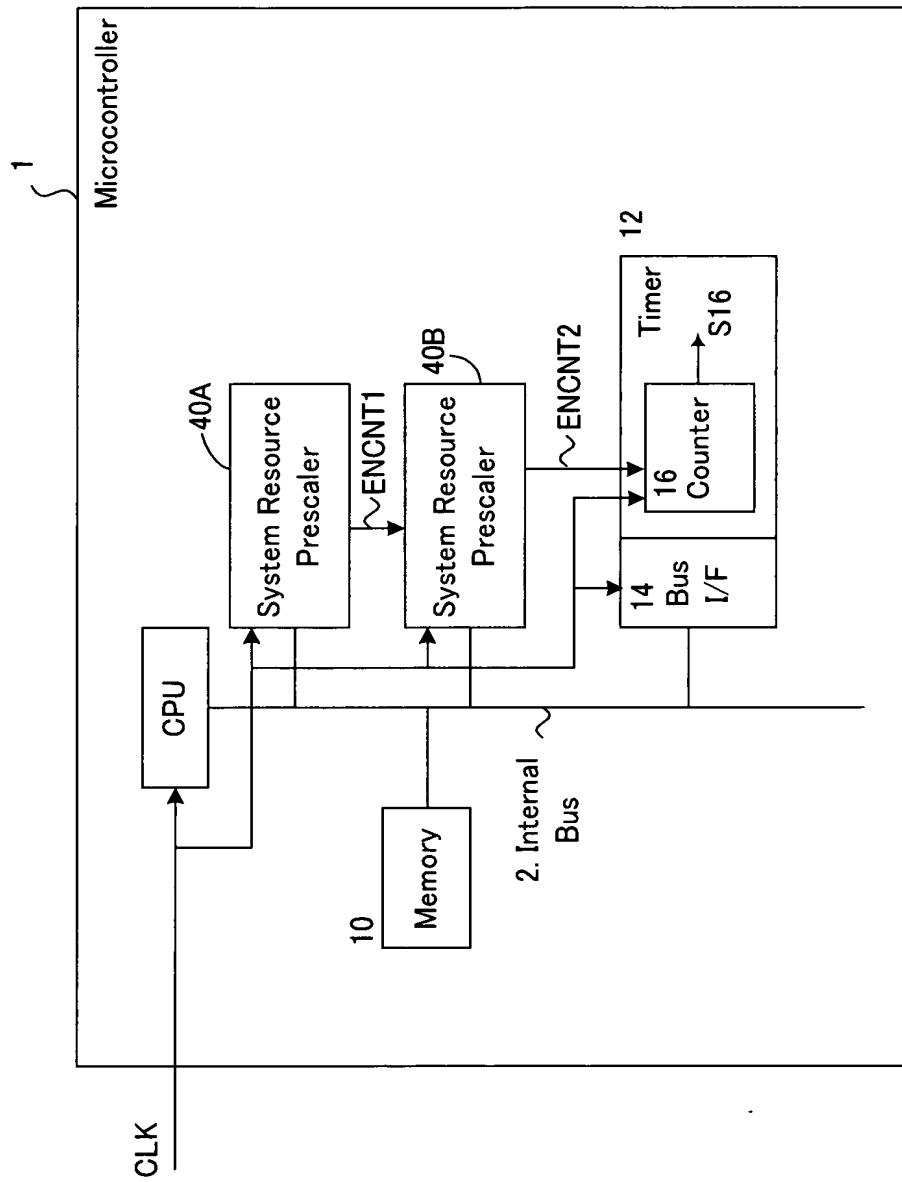


FIG. 9

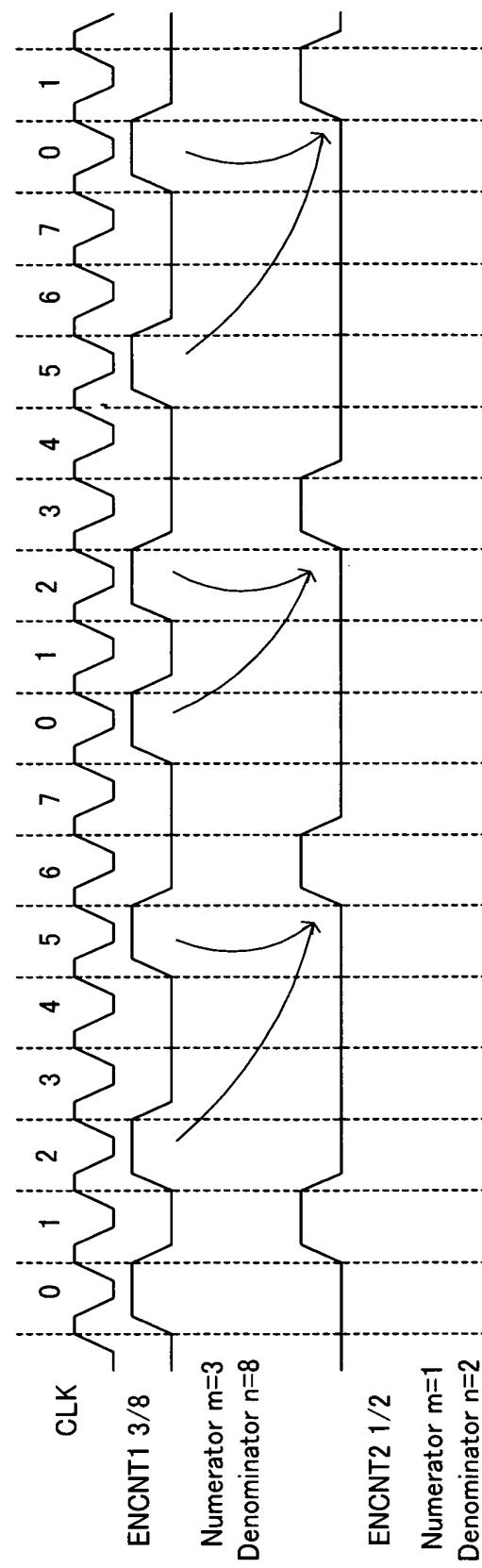


FIG. 10

Example of Two-Stage Prescaler

